



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application: Hsieh

Application No.: 09/870,566

Filed: May 31, 2001

Title: **System and Method for Split Lot ID Naming**

Commissioner for patents
Washington, D.C. 20231

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POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
(REVOCATION OF PRIOR POWERS)

As assignee of record of each of the below-listed patent applications,

No.	Serial No.	<u>Title</u>	<u>Filing Date</u>
1	09/642,500	Method and Apparatus for Preventing Contamination During a Process Flow	08/18/2000
2	09/649,706	Method for Remote Mask Data Jobview Through a Network	08/25/2000
3	09/887,980	SCR Device with Deep-N-Well Structure for On-Chip ESD Protection Circuits	06/22/2001
4	09/870,566	System and Method for Split Lot ID Naming	05/31/2001
5	09/659,923	Substrate-Triggered Technique for On-Chip ESD Protection Circuit	09/12/2000

REVOCATION OF PRIOR POWERS OF ATTORNEY

all powers of attorney previously given in each of the listed patent applications are hereby revoked, and

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NEW POWER OF ATTORNEY

the following attorneys/agents are hereby appointed to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith: I hereby appoint all attorneys of Thomas, Kayden, Horstemeyer & Risley, LLP, who are listed under the USPTO Customer Number shown below as the attorneys to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith, recognizing that the specific attorneys listed under that Customer Number may be changed from time to time at the sole discretion of Thomas, Kayden, Horstemeyer & Risley, LLP, and request that all correspondence about the application be addressed to the address filed under the same USPTO Customer Number.

24504

Patent Trademark Office

Please direct all correspondence and telephone calls to:

Daniel R. McClure, Reg. No. 38,962
THOMAS, KAYDEN, HORSTEMEYER & RISLEY, LLP
100 Galleria Parkway, Suite 1750
Atlanta, Georgia 30339
770-933-9500

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ASSIGNEE OF ENTIRE INTEREST

TAIWAN SEMICONDUCTOR MANUFACTURING CO., LTD
121, Park Ave. III, Science-Based Industrial Park,
Hsin-Chu 300, Taiwan, R.O.C.

ASSIGNEE CERTIFICATION

The certification under 37 C.F.R. §3.73(b) establishing the right of assignee to take action is attached hereto along with documentation evidencing same.

Date: Oct. 2, 2002

Da-Chi Lin
Deputy Director

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Statement Under 37 C.F.R. 3.73(b)

Taiwan Semiconductor Manufacturing Co., Ltd., of 121, Park Ave. III, Science-Based Industrial Park, Hsin-Chu 300, Taiwan, R.O.C., is the assignee of the entire right, title, and interest in each of the below-listed patent applications, which assignments were recorded in the United States Patent & Trademark Office at the Reel and Frame noted for each application listed (by Serial Number, Title, and Filing Date) in the table below:

No.	Serial No.	Title	Filing Date	Assignment (Reel/Frame)
1	09/642,500	Method And Apparatus For Preventing Contamination During A Process Flow	08/18/2000	011025/0731
2	09/649,706	Method For Remote Mask Data Jobview Through A Network	08/25/2000	011042/0194
3	09/887,980	SCR Devices With Deep-N-Well Structure For On-Chip ESD Protection Circuits	06/22/2001	011936/0356
4	09/870,566	System And Method For Split Lot ID Naming	05/31/2001	011876/0128
5	09/659,923	Substrate-Triggered Technique For On-Chip ESD Protection Circuit	09/12/2000	011099/0753

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

Date: Oct. 2, 2002

Signature: [Handwritten Signature]

Typed or printed name: Da-Chi Lin

Title: Deputy Director

The extent (by, percentage) of Taiwan Semiconductor Manufacturing Co., Ltd's ownership interest is 100%.

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